

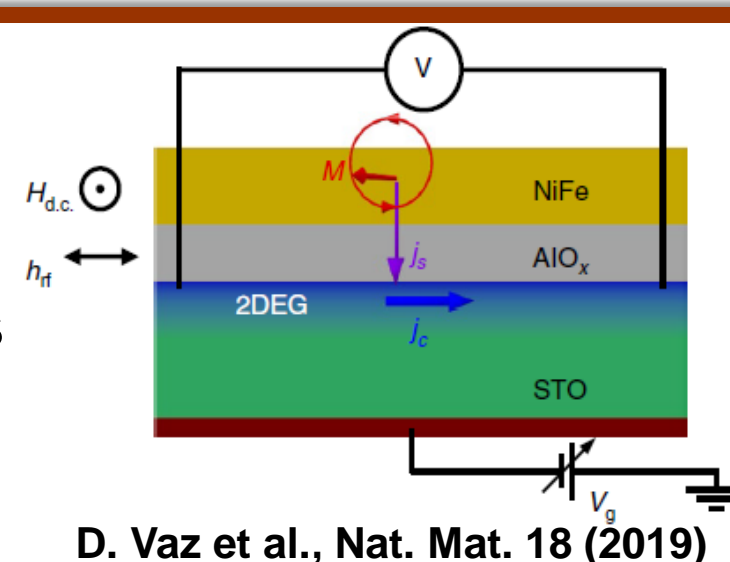
ABSTRACT

- We study spin-orbit torques by spin-torque ferromagnetic resonance (ST-FMR)
- Non-volatile electric-control of the 2D electron gas (2DEG) resistivity is demonstrated in ST-FMR devices
- Metal-barrier thickness and temperature dependences of the charge-to-spin conversion in Metal/SrTiO₃ systems are reported

2DEG in SrTiO₃ systems

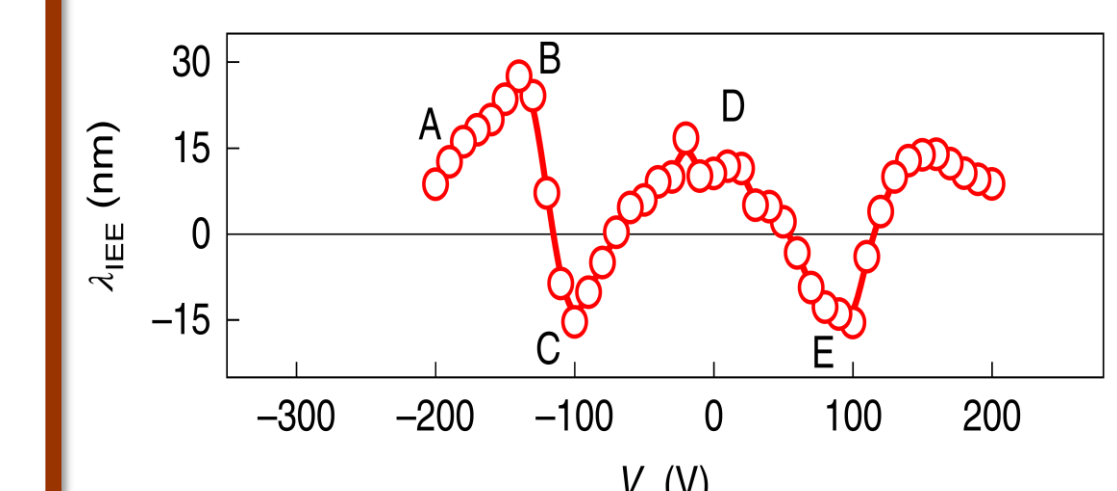
2DEG at metal/STO interface

- Possibility to tune the 2DEG properties
- Back-gate voltage (V_g) modulation
- Influence on spin-charge interconversion rates



D. Vaz et al., Nat. Mat. 18 (2019)

Large spin-to-charge conversion

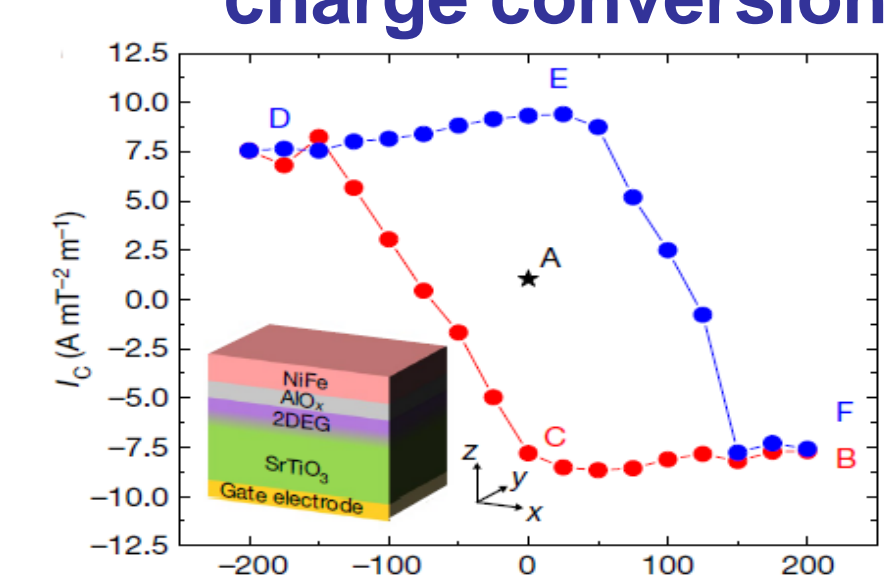


High Efficiency:
 $\lambda_{\text{NIE}} \sim 30 \text{ nm}$
(0.2 nm in Pt)

Spin-pumping measurements: Back-gate voltage modulation in oxide 2DEG

D. Vaz et al., Nat. Mat. 18 (2019)

Non-volatile electric-control of the spin-charge conversion

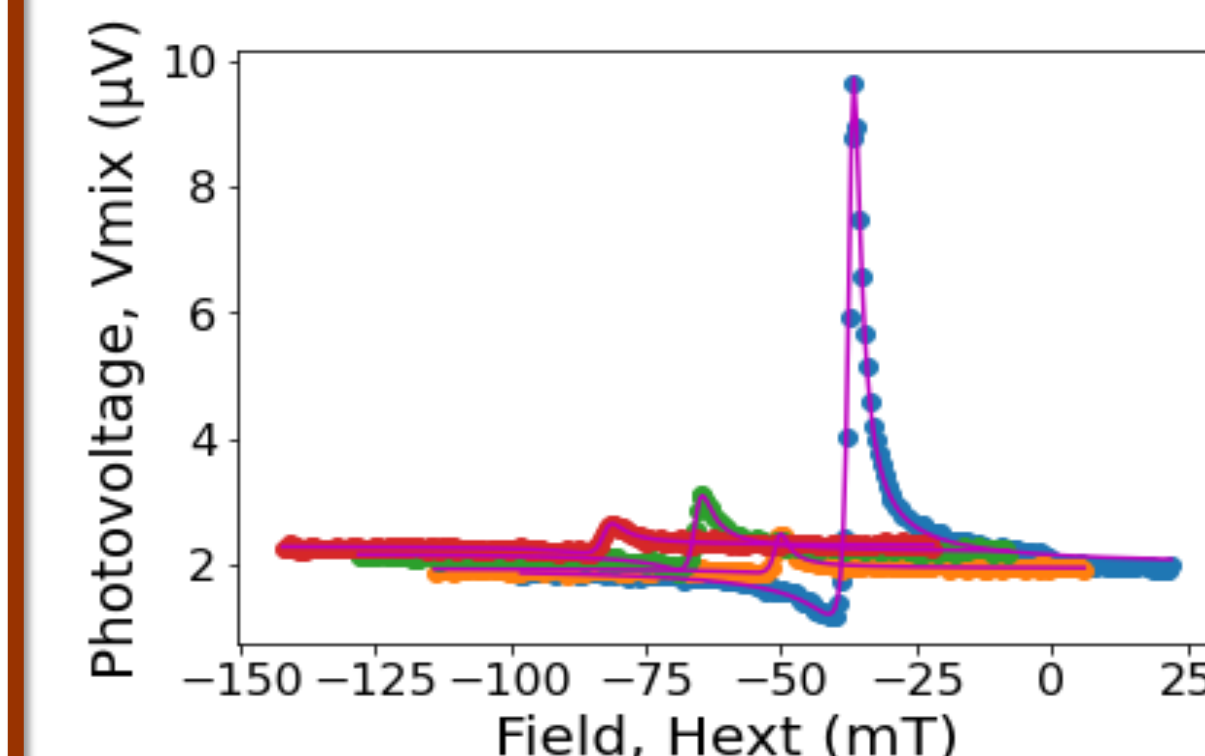


Ferroelectric phenomenon

P. Noel et al., Nat. 580 (2020)

Metal barrier thickness dependence at 10K

STO/Ta(0.6nm)/NiFe(10nm)/MgO/Ta

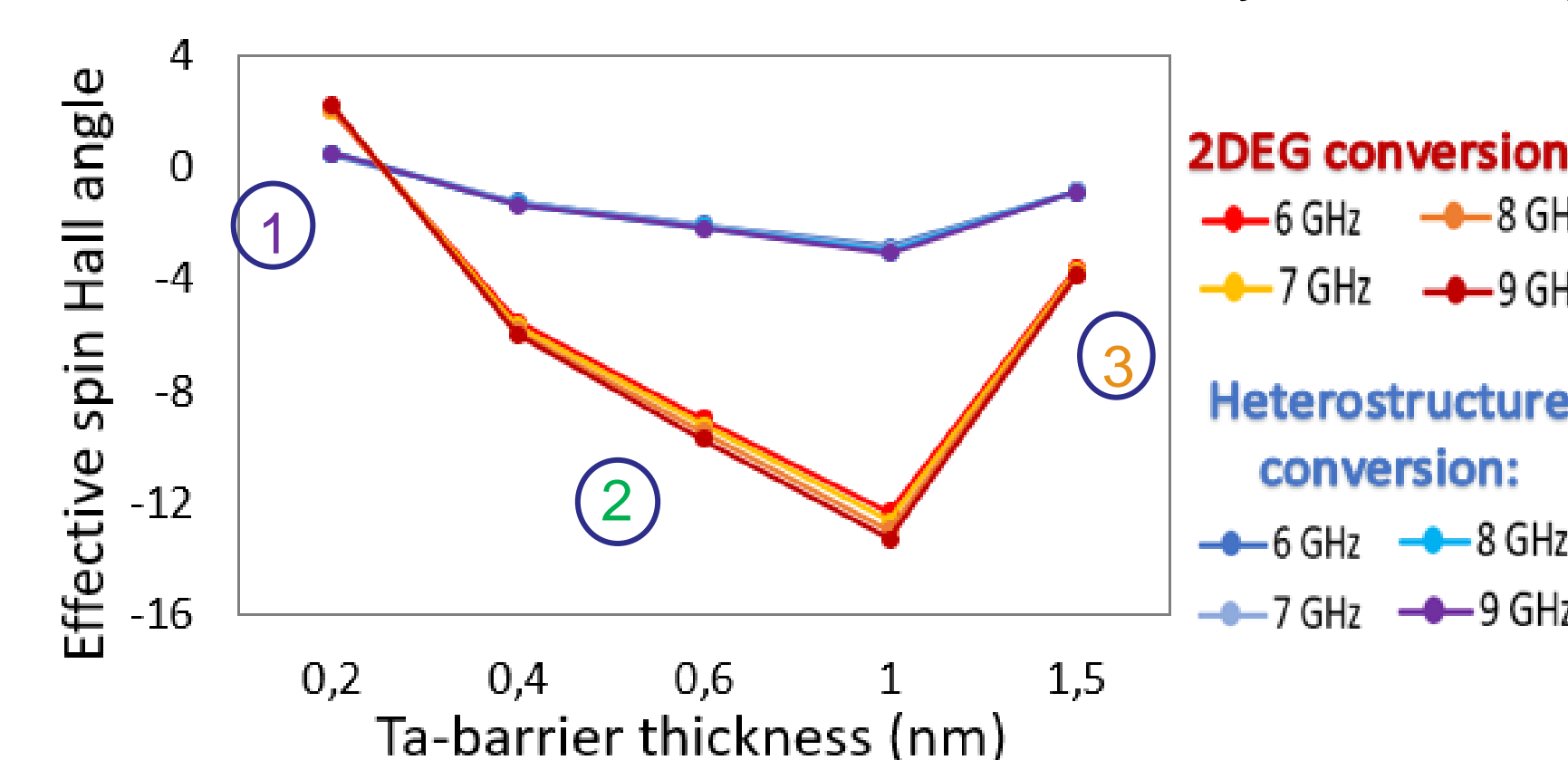
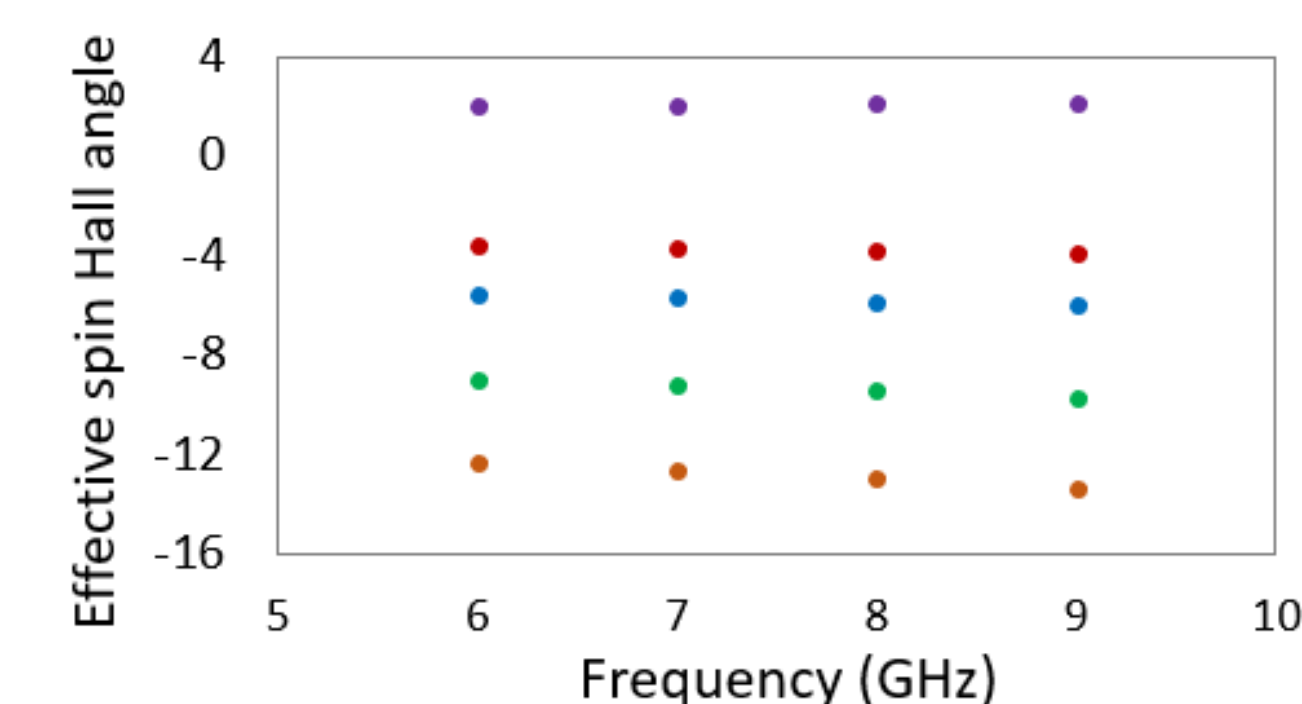


- 6 GHz
- 7 GHz
- 8 GHz
- 9 GHz

ST-FMR signals: Lorentzian functions

Analysis method: modulation damping*. Effective spin Hall angle Θ_{tot}^* extracted from the linear dependence of the damping α with the injected DC charge current

Ta-barrier thickness dependence



2DEG conversion: Effective spin Hall angle versus frequency for different Ta-barrier thicknesses

Ta-barrier thickness dependence of the effective spin Hall angle in the 2DEG and in the whole heterostructure (2DEG + Ta/Py interface)

From magnetotransport measurements at 10K, we have $R_{\text{NiFe}} = 90.2 \Omega$ and $R_{\text{tot}} = 80.2 \Omega \rightarrow R_{\text{2DEG}} = 722.4 \Omega$

Portion of charge current passing through the 2DEG is: $\frac{R_{\text{NiFe}}}{R_{\text{NiFe}} + R_{\text{2DEG}}} = 11.1\%$

At least 2 charge-to-spin conversion contributions: 1) in the 2DEG 2) at the Ta/Py interface

1) STO/Ta/NiFe reference sample without 2DEG. Ta layer is not thick enough to enable 2DEG formation.

2) For $t_{\text{Ta}} = 0.4, 0.6$ and 1 nm , Ta layer thickness thick enough to allow 2DEG formation \rightarrow strong contribution of the 2DEG. Ta layer is thin enough to have a weak barrier effect. Both 2DEG and Ta(oxidized)/Py interface contribute to the conversion.

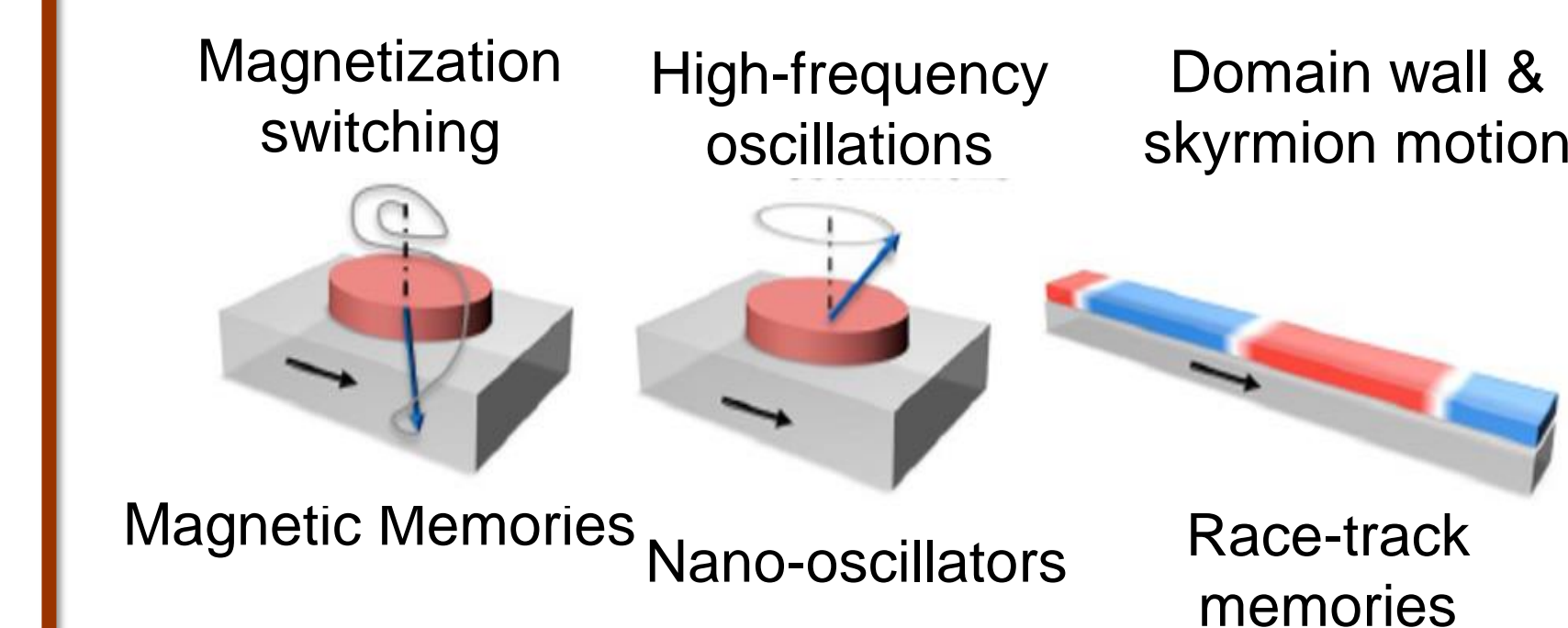
3) High 2DEG contribution to the conversion but thick Ta layer \rightarrow possible strong barrier effect. Only small part of the conversion in the 2DEG is detected, together with lower conversion at Ta(oxidized)/Py interface due to lower Ta oxidation

*L. Liu et al., Phys. Rev. Lett. 106 (2011)

Spin-orbit torques in MRAMs

- Current-induced magnetization switching for memory applications (SOT-MRAMs)
- For now: memory applications made of spin Hall materials \rightarrow large SOT in heavy metals
- Drawback: SOT sign & conversion efficiency are fixed by materials

Electric-control in STO = new degree-of-liberty \rightarrow SOT efficiency modulation



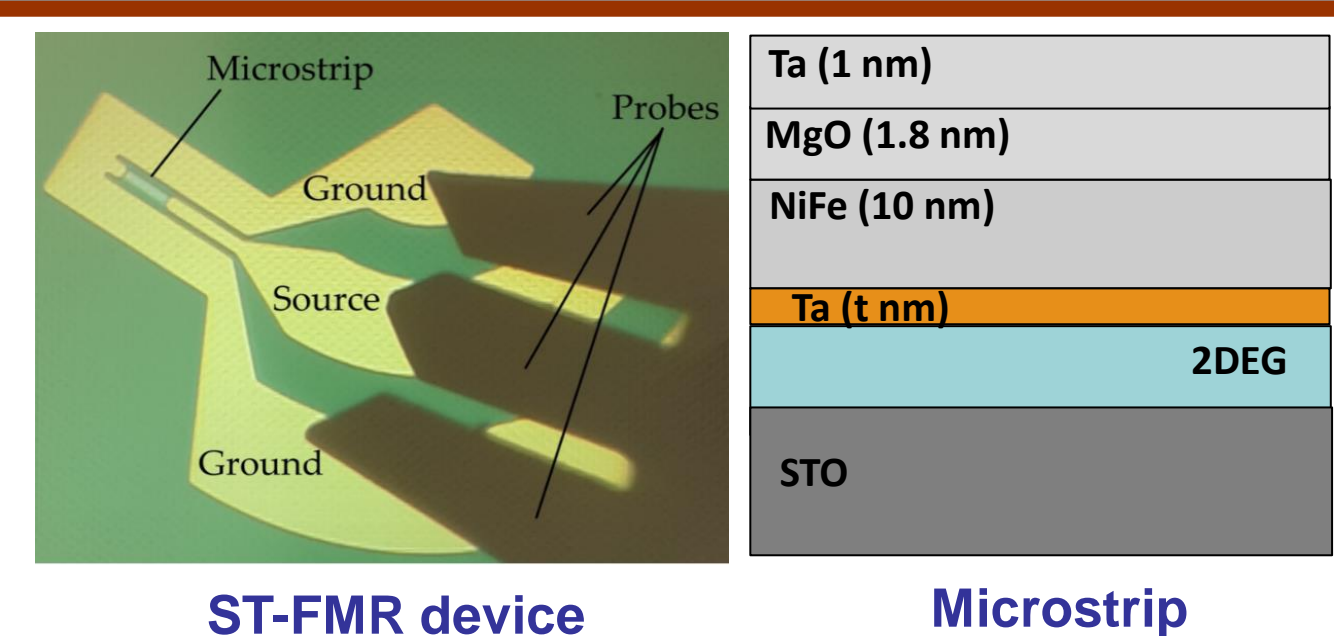
SOT - MRAMs

M. Kharbouche-Harrari et al., Conference on Design of Circuits and Integrated Systems (2018)

ST-FMR in STO

- Ferromagnetic resonance driven by spin-orbit torques
- Coplanar waveguide matched to STO dielectric constant
- Spin-orbit torques: - Field-induced torque
- Damping-like torque
- Field-like torque

Due to Spin Hall or Rashba-Edelstein effects



ST-FMR device

Microstrip

CONCLUSIONS

- Spin-orbit torques generated via the ST-FMR technique
- Formation of the 2DEG at Ta/STO interface
- Increasing 2DEG conductivity with decreasing temperature
- Non-volatile electric-control of the 2DEG resistivity by applying a back-gate voltage

Optimum charge-to-spin conversion for $t_{\text{Ta}} = 1 \text{ nm}$. Good balance required:

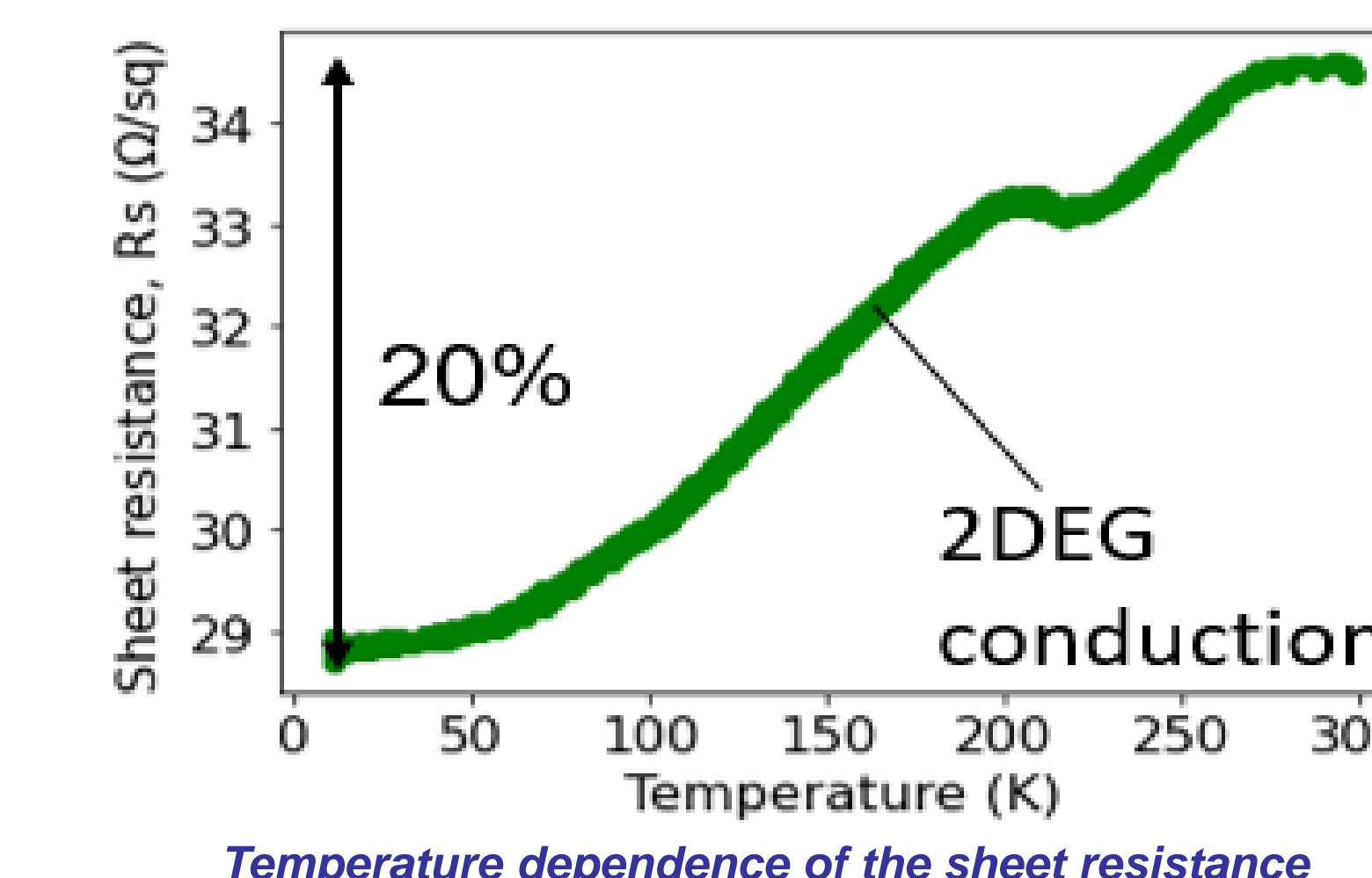
- if Ta layer is too thin (ex 0,2 nm): no 2DEG creation
- If Ta layer is too thick (ex: 1,5 nm): barrier effect
- Effective spin Hall angle increases with increasing barrier thickness from 0.4 to 1 nm
- Possible better detection of the charge-to-spin conversion in the 2DEG due to electron tunneling through the Ta-barrier for increasing temperature

ACKNOWLEDGEMENTS

The devices were fabricated in the Plateforme Technologie Amont in Grenoble, and we acknowledge the support of the Renatec network. This work was supported by the "ANR-Contrabass".

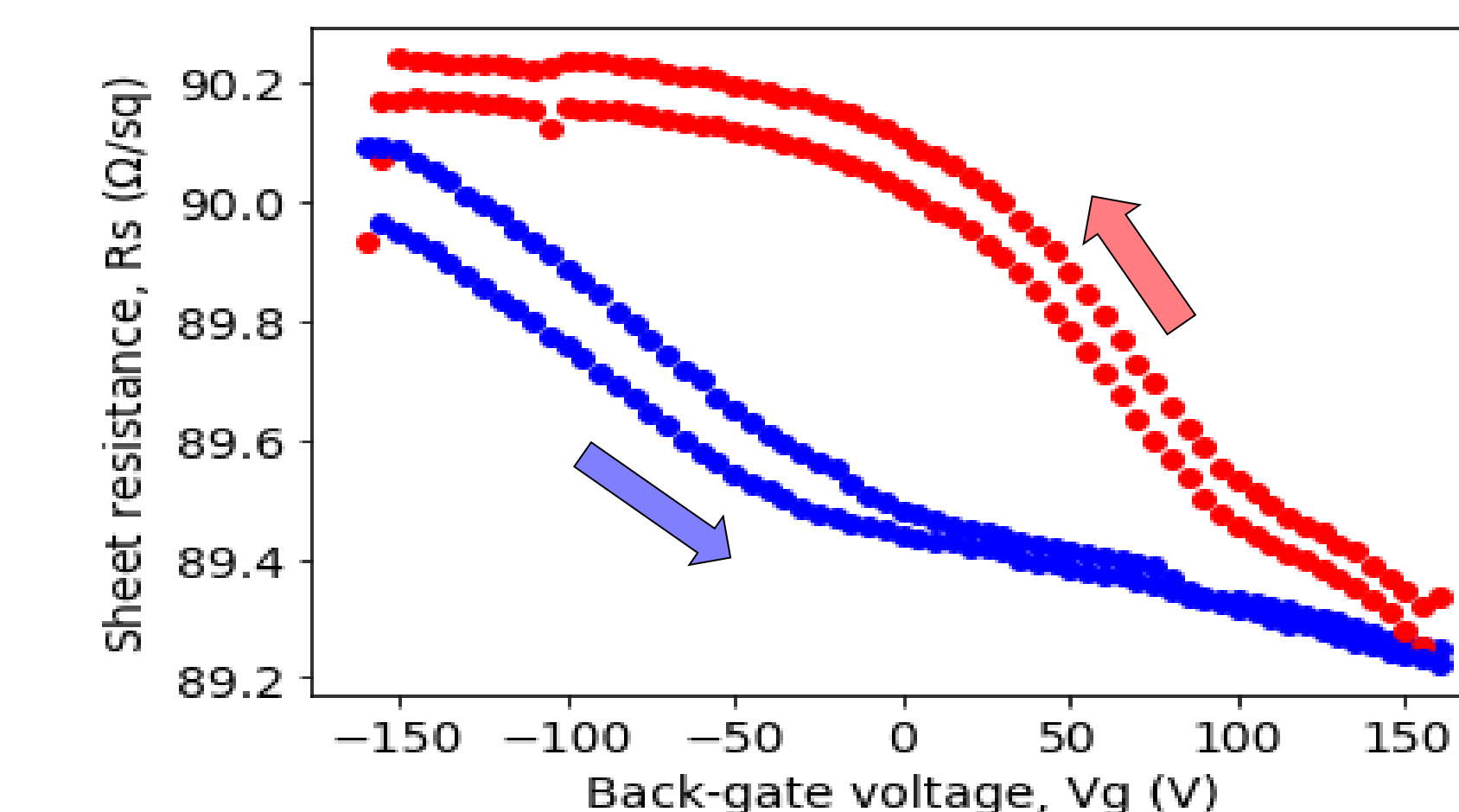
2DEG at Ta/STO interfaces

STO/Ta(1nm)/NiFe(10nm)/MgO/Ta



Temperature dependence of the sheet resistance

Decreasing sheet resistance with decreasing temperature \rightarrow signature of the 2DEG

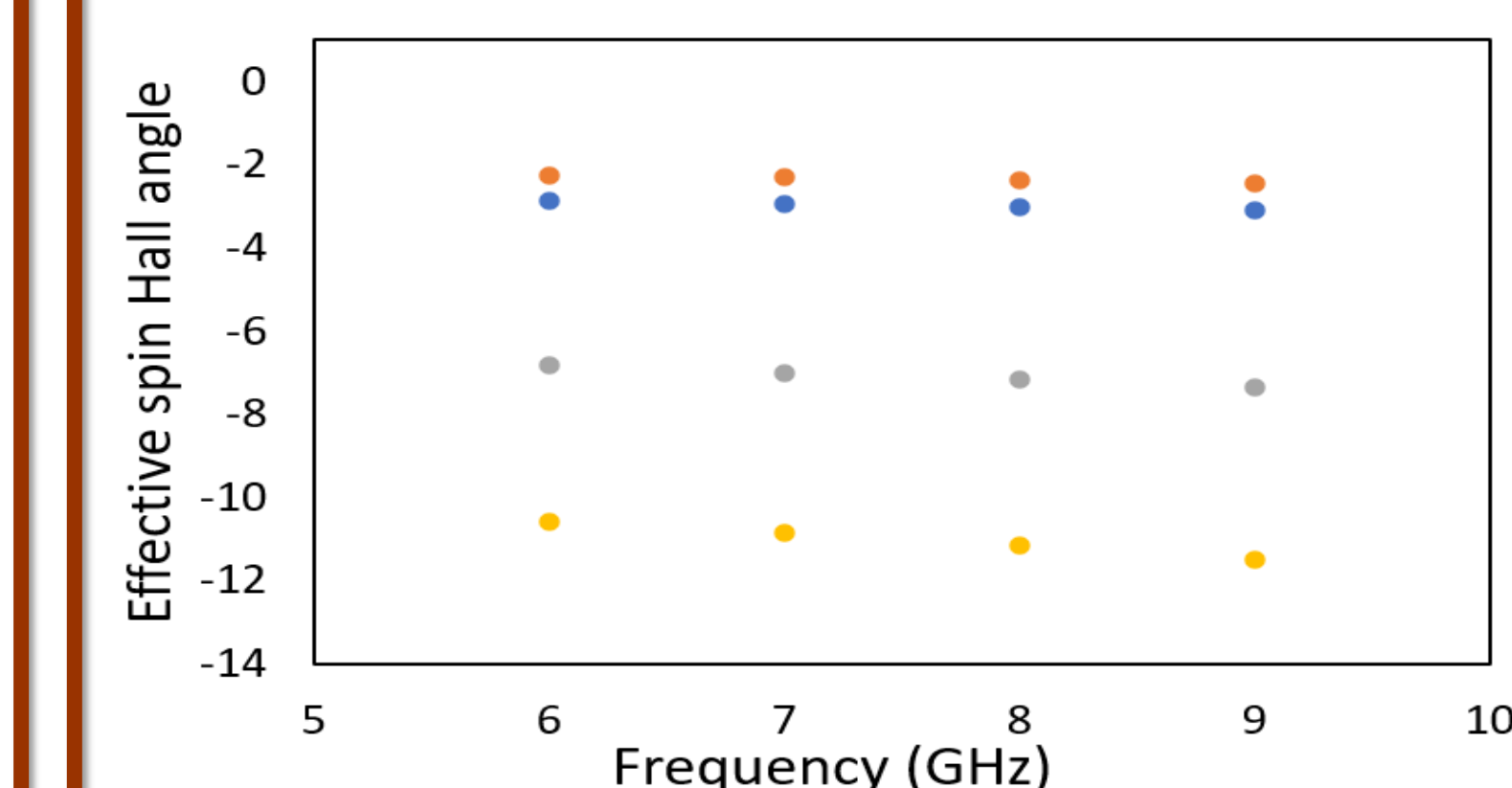


Non-volatile electric-control of the sheet resistance of the 2DEG in ST-FMR devices at 10K

- 2 resistance states:
 - Lower state = electron enrichment
 - Higher state = electron depletion
- Inversed hysteresis cycle \rightarrow Charge trapping

Preliminary results on temperature dependence

STO/Ta(1nm)/NiFe(10nm)/MgO/Ta



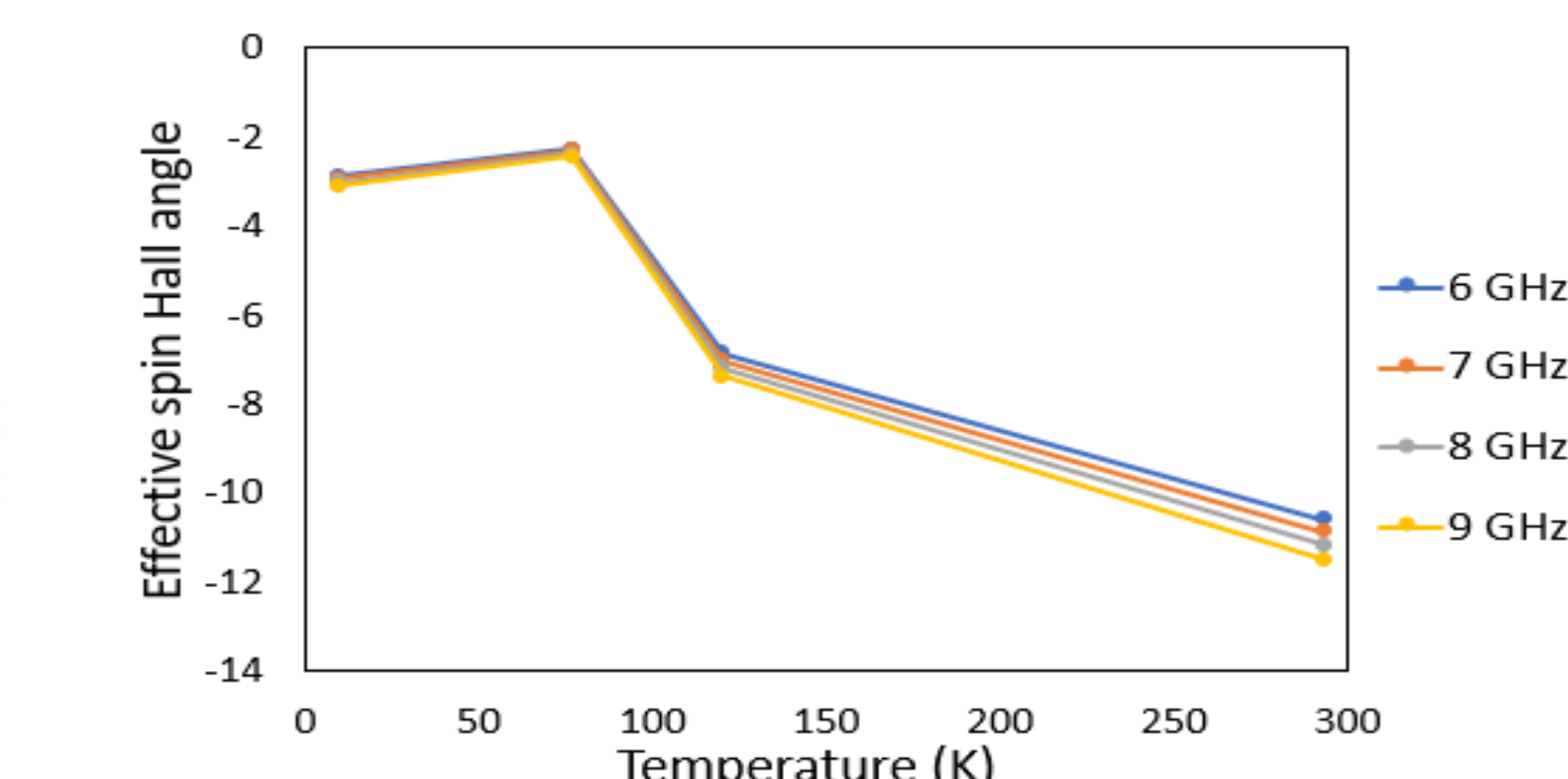
Heterostructure conversion: Effective spin Hall angle versus frequency for different temperatures

When temperature \nearrow :

\rightarrow 2DEG conductivity \searrow (Fig.1)

\rightarrow BUT electron energy $\nearrow \rightarrow$ electron tunneling* through the Ta-barrier

\rightarrow Possible better detection of the charge-to-spin conversion in the 2DEG



Temperature dependence of the heterostructure charge-to-spin conversion

* Y. Wang et al., Nano Lett. 17 (2017)